



PATENT
Customer No. 22,852
Attorney Docket No. 08245.0027

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
)
Jun Dong KIM et al.) Group Art Unit: 2814
)
Application No.: 09/892,878) Examiner: Shrinivas H. Rao
)
Filed: June 28, 2001)
)
For: METHOD FOR FORMING GATE)
ELECTRODES IN A)
SEMICONDUCTOR DEVICE USING)
FORMED FINE PATTERNS)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

PETITION FOR EXTENSION OF TIME

Applicants hereby petition for a two-month extension of time to reply to the Office
Action of March 31, 2004. A fee of \$420.00 is enclosed.

Please grant any extensions of time required to enter this response and charge any
additional required fees to our deposit account 06-0916.

Respectfully submitted,

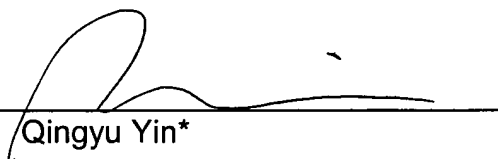
FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

09/01/2004 CNGUYEN 00000061 09892878

02 FC:1252

420.00 DP

Dated: August 31, 2004

By: 
Qingyu Yin*

*With limited recognition under 37 C.F.R. § 10.9(b)